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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10019437	FILING DATE 12/31/2001	CLASS 349	SUBCLASS 87	GAU 2821	EXAMINER NELSON
**APPLICANTS: Doi Yasuyuki; Oomori Tetsuro; Nishi Kazuyoshi; 2675					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/05904 08/31/2000					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 11/356893 12/16/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RES-IND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 6J188-124	
Verified and Acknowledged Examiners's initials					
TITLE : Liquid crystal drive circuit, semiconductor integrated circuit device, reference voltage buffer circuit, and method for controlling the same					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-4361 (Rev. 12-94)					

Best Available Copy

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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